

## Information Sheet: ONE Workbench for IPC@CHIP®

The state-of-the-art software development environment for the IPC@CHIP®



The ONE Workbench for IPC@CHIP® is a comfortable integrated development environment (IDE) for creating C and C++ applications for the IPC@CHIP®. It includes the C/C++ compiler for the IPC@CHIP® SC2x3. The compiler and its libraries are for the most part C-Source compatible to those of the Paradigm Beck IPC Edition (SC1x, SC2x and SC1x3).

The workbench is based on the common Eclipse framework and the CDT C/C++ editor plug-in. It benefits from a large user community which results in plenty of features for modern and comfortable software development. Here are some of the provided features:

### C/C++ Source Browser

The editor allows comfortable browsing of C/C++ sources. It lists all symbols (variables, functions, classes, etc.) of a source file. The navigator opens declarations and definitions of symbols and jumps back or forward to last edit points.

### Code Completion

The code completion completes symbols or phrases during typing. It completes known functions, elements of classes and structures, preprocessor macros etc.

### C/C++ Syntax Checking

The editor checks the syntax of C/C++ code during typing. Thus most compile errors are detected already during editing. Errors and warnings will be marked on the editors' scrollbar which allow an easy survey of erroneous code.

### To-Do Manager

The To-Do Manager collects all comments which are marked with a To-Do note. The collection is then summarized in a global To-Do table.

### Local History

For each source file a local history is provided. This allows analyzing modifications and stepping back to old versions of the file.

### Remote Debugger

For target debugging, the workbench supports an integrated remote debugger with a comfortable graphical user interface.

### Remote System Explorer

The remote system explorer accesses the target via telnet, SSH and FTP. This allows downloading of applications and access to the command shell of the IPC@CHIP® RTOS.

### RTOS API documentation and C library

The IPC@CHIP® RTOS API documentation is incorporated in the Help system of the development environment. The Beck API C libraries are already integrated and available to the user for developing applications.

### Minimum system requirements

- PC with Pentium III processor
- Microsoft Windows XP or Vista
- 512 MByte RAM
- 512 MByte hard disk memory

### Support - We are always there for you!

Technical support for this product is provided exclusively by Beck IPC GmbH. Users may also take advantage of our newsgroup forum for exchanging information with other users at: <http://www.beck-ipc.com/forum>.

### License

The Beck ONE Workbench generates code for IPC@CHIP® Controllers exclusively. Other controllers are not supported.

Further information: <http://www.beck-ipc.com>